



Srishyla Educational Trust (R), Bheemasamudra  
**GM INSTITUTE OF TECHNOLOGY, DAVANGERE**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**RESEARCH CENTRE**



**Research Scholars Registered Under E&CE Dept. Research Center**

Sl. No	Name of the Research Guide	Research Scholar	Title	University	Year of Registration	No. of Candidate Registered for		Status			Papers Published
						Ph.D.	M.Sc. (Engg.)	Course work Completion Y/N	Compr. Viva-Voce Y/N	Thesis Submitted Y/N	
1	<b>Dr. S H Hiremath</b>	<b>Mr.Umeshkumar C Hirematha</b>	Development of a software tool for analysis and decoding of narrowband data protocols using C and DSP Processor	VTU	2012	✓	-	Y	Y		-
2	<b>Dr. Chandrappa D.N</b>	<b>Mr. Rajendra Soloni (4GM16PEJ01)</b>	Some Studies on Independent Frequency and Radiation Pattern Reconfigurable Microstrip Antennas for Wireless Communication	VTU	2015	✓	-	Y	Y	N	01
		<b>Mr. Rajappa H S (4GM17PEA04)</b>	Design and Analysis of Miniaturized	VTU	2017	✓	-	Y	N	N	01

			Reconfigurable Ultra Wideband Slotted Micro Strip Antenna for Wireless Communication								
		<b>Mr. Chetan S (4GM17PEA02)</b>	Design and Development of Compact Wide and Multiband frequency Reconfigurable Microstrip Planar Monopole Antenna for Wireless Applications	VTU	2017	✓	-	N	N	N	-
3	<b>Dr. Hadimani H C</b>	<b>Mr. Shakib Ahmed Ali (BL16PHNT011)</b>	Development of Piezoresistive Microcantilever Sensors for Microsensing Applications	VTU	2017	✓	-	N	N	N	-
		<b>Mr.Hameed Pasha Mohammed (BLPHDEL438)</b>	Design of Distinct P-based Content Addressable Memory Using Double Gate MOSFET (DGMOSFET)	VTU	2017	✓	-	N	N	N	-