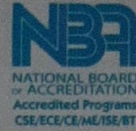




Srishyla Educational Trust (R), Bheemasamudra

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

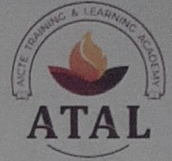
Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka



Department of Electronics and Communication Engineering



AICTE ATAL SPONSORED TWO WEEKS FDP ON



On

“Analog and Digital Design Flow for VLSI Chip Application”

in association with Entuple Technologies, Bengaluru



20th to 25th February 2023

27th Feb – 03rd March 2023

ATAL FDP REPORT

Harish G C
Coordinators

Mr. Harisha G C

Mr. Sampath Kumar B

Assistant Professor

Dr. Praveen J
Verified By

Head of the Department

Dept. of Electronics & Comm. Engg.

GM Institute of Technology

DAVANGERE-577 006

Dr. Sunil Kumar B S

Checked by

Dr. Sunil Kumar B S

DEAN ACADEMICS

GM Institute of Technology

DAVANGERE-577006.

Dr. Praveen J
Checked by

Dr. Praveen J

IQAC Director

Dr. Praveen J

GM Institute of Technology

Davangere - 577 006.

Dr. Sanjay Pande M B
Approved by

Dr. Sanjay Pande M B

Principal
PRINCIPAL

GM Institute of Technology

Davangere - 577 006.



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ATAL sponsored two weeks FDP on **“Analog and Digital Design Flow for VLSI Chip Application”** was conducted from 20th to 25th FEB 2023 in online mode and 27th FEB to 3rd MAR 2023 in offline mode in which around 49 participants from different parts of the country participated.

The speakers for the two weeks FDP on **“Analog and Digital Design Flow for VLSI Chip Application”** were:

RESOURCE PERSON DETAILS

Dr. Praveen J – Ph.D. in the field of Low Power VLSI design, currently he is working as IQAC Director, Professor & Head, Department of ECE at GM Institute of Technology. His area of research includes frontend and backend design of low power VLSI circuit, CMOS circuit and good at HDL Programming. He has more than two decades of teaching experience and served as HOD, Dean Academics in various arena of teaching field.

Mr. Anikethan H V U - Presently working as co-founder and COO of CoachEd, Bangalore. He has worked in Qualcomm and ARM. His area of expertise is Design of CMOS VLSI digital circuits for high performance and Low power. In Qualcomm, he has designed and implemented Duty Cycle measurement and correction circuit, Clock Jitter and Voltage Droop Measurement circuit, Low power flipflops design. He has strong technical Background with B. Tech from NITK Surathkal and author of a paper on FPGA in IEEE conference.

Arun Iyer - Principal Engineer currently Leading the FPGA physical architecture team in AMD Xilinx India. 20 years of experience, worked with Qualcomm, Texas Instruments, AMD in the past With more than 10 patents in VLSI domain, he is technically sought after Expertise in High Performance and Low power circuit design.



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Mr. Mamidi Nagaraju - A total of 12 years of working experience. started my career as SMDP-II Lab Engineer at NIT Calicut in 2009, later Application Engineer at CoreEL Technologies Bangalore for 4 years and then MeitY, Govt. of India, Sponsored Project titled "SMDP-Chip for System Design" at M.N. NIT Allahabad as Project Engineer from 2016 to 2021. In the same project.

Mr. Sudhir Kumar - presently working as Fied Application Engineer at Entuple Technologies has an experience of about 10 years in the field of VLSI Analog Design. His area of Specialization is layout design for Analog, VLSI and CMOS circuits and also, he has supporting many customers in the career of Analog Design.

Mr. Shrinivas Bhat - Presently working as Analog circuit design engineer at Entuple technologies, Bangalore. His area of expertise is Analog IP design for Analog, VLSI and CMOS circuits and also, he has supporting many customers both in India and abroad in the area of IP Analog design. He has more than one decade of industry experience.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**LIST OF PARTICIPANTS**

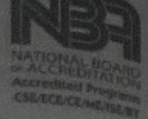
Sl. No.	Participant Name	Name of the Institute
1	Mr. Mahendra Shridhar Naik	NEW HORIZON COLLEGE OF ENGINEERING
2	Mr. Ravi Rayappa	JAIN INSTITUTE OF TECHNOLOGY DAVANGERE
3	Mr. Arun Raj S R	UNIVERSITY B.D.T COLLEGE OF ENGINEERING
4	Dr. Rooban S	KL UNIVERSITY
5	Dr. Nirmala S O	BIET , DAVANGERE
6	Mrs. Savithri G. R.	BAPUJI INSTITUTE OF ENGINEERING AND TECHNOLOGY
7	Mr. Shivayogappa HJ	PESITM SHIVAMOGGA
8	Mr. Sharanabasaveshwara	JAIN INSTITUTE OF TECHNOLOGY, DAVANGERE
9	Mr. Ravikumar K I	JAIN INSTITUTE OF TECHNOLOGY
10	Mrs. Vanishree H V	BIET
11	Mr. Pradeepa S C	JNNCE, SHIMOGA
12	Mr. Amit Kumar K	PES INSTITUTE OF TECHNOLOGY AND MANAGEMENT, SHIMOGGA
13	Dr. Madhavi	PES INSTITUTE OF TECHNOLOGY AND MANAGEMENT
14	Dr. Leela G H	BAPUJI INSTITUTE OF ENGINEERING & TECHNOLOGY
15	Mr. Praveen K B	DR. AMBEDKAR INSTITUTE OF TECHNOLOGY
16	Mr. Mukundswamy MS	DR. AMBEDKAR INSTITUTE OF TECHNOLOGY
17	Dr. Shivaputra	DR. AMBEDKAR INSTITUTE OF TECHNOLOGY
18	Mr. Rajappa H S	ADICHUNCHANAGIRI INSTITUTE OF TECHNOLOGY,
19	Mr. Chandra Shekar P	VISVESVARAYA TECHNOLOGICAL UNIVERSITY
20	Mr. Santhosh B G	G M INSTITUTE OF TECHNOLOGY, DAVANGERE
21	Miss. Deepika V B	G M INSTITUTE OF TECHNOLOGY, DAVANGERE
22	Mrs. Veena H T	STJIT, RANEBENNUR
23	Mr. Manjunatha Y	G M INSTITUTE OF TECHNOLOGY, DAVANGERE
24	Mr. Chetan B V	G M INSTITUTE OF TECHNOLOGY, DAVANGERE
25	Mrs. Chaitra S N	NMAMIT NITTE
26	Mr. Suprith PG	REVA
27	Dr. Kavitha K J	G M INSTITUTE OF TECHNOLOGY, DAVANGERE
28	Mr. Hanumanth Raju R K	JAIN UNIVERSITY
29	Mrs. Deepa. T	VISVESVARAYA TECHNOLOGICAL UNIVERSITY
30	Mrs. Revathi S N	UBDT COLLEGE OF ENGINEERING, DAVANAGERE
31	Mrs. Akshata Chavan	SDMCET DHARWAD



Srishya Educational Trust (R), Bheemasamudra

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka

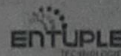


DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

32	Mrs. Priyanka C N	BIET, DAVANAGERE
33	Mr. Rakesh Annigere	SJVP
34	Mr. Aruna Kumar B T	UBDT COLLEGE OF ENGINEERING, DAVANAGERE
35	Mr. Chetan S	SJM INSTITUTE OF TECHNOLOGY CHITRADURGA
36	Mrs. Farzana Parveen BA	SJMIT, CHITRADURGA
37	Dr. Hamsa S	JYOTHY INSTITUTE OF TECHNOLOGY
38	Dr. Neelambike S	MALNAD COLLEGE OF ENGINEERING
39	Mrs. Niju Rajan	NMAMIT
40	Mrs. Charishma	NMAMIT
41	Dr. Vikash Kumar	RV INSTITUTE OF TECHNOLOGY AND MANAGEMENT
42	Miss Farah Naz	RAJ KUMAR GOEL INSTITUTE OF TECHNOLOGY
43	Dr. Archana Kanwar	LOVELY PROFESSIONAL UNIVERSITY
44	Mr. Jitendra Kumar Singh	BTIRT SAGAR
45	Miss Bolleddu Alekya	PACE ITS
46	Mr. Abhishek Srivastava	SRMSCET BAREILLY
47	Dr. Ajaykumar Patel	KINHAVALI COLLEGE
48	Miss. Rashi Sharga	SHRI GOVINDRAM SEKSARIA INSTITUTE OF TECHNOLOGY AND SCIENCE, INDORE
49	Mr. Harvinder Singh	CHANDIGARH UNIVERSITY

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING****ONLINE MODE: 20th to 25th FEB 2023**

AICTE TRAINING AND LEARNING (ATAL) ACADEMY
Sponsored Two week
Faculty Development Program on
"Analog and Digital Design Flow for VLSI Chip Application"
Organized by



Department of Electronics and Communication Engineering
G M INSTITUTE OF TECHNOLOGY, Davangere-06
27th Feb to 2nd March 2023 - Offline Mode

Week - 1 ONLINE MODE (7.00 PM to 9.30 PM)

Dates	7.00 PM to 7.50 PM	8.00 PM to 8.50 PM	9.00 PM to 9.30 PM
20/02/2023	Session - 1 (R) VLSI Design Flow : ASIC and FPGA	Session - 1 (RD) VLSI Design Flow : ASIC and FPGA	Session - 1 Interaction using Q&A
21/02/2023	Session - 2 (R) High level of Digital Design : Abstraction, Modularity, Regularity	Session - 2 (RD) High level of Digital Design : Abstraction, Modularity, Regularity	Session - 2 Interaction using Q&A
22/02/2023	Session - 3 (R) Combinatorial Logic Design : Optimization for PPA, Noise margin, Characterization, Sizing	Session - 3 (RD) Combinatorial Logic Design : Optimization for PPA, Noise margin, Characterization, Sizing	Session - 3 Interaction using Q&A
23/02/2023	Session - 4 (R) Sequential Logic Design : Optimization for PPA, Characterization, Sizing, Timing Analysis	Session - 4 (RD) Sequential Logic Design : Optimization for PPA, Characterization, Sizing, Timing Analysis	Session - 4 Interaction using Q&A
24/02/2023	Session - 5 (R) PVT : Concepts of process, variation, voltage temperature and it's effect on Yield	Session - 5 (RD) PVT : Concepts of process, variation, voltage temperature and it's effect on Yield	Session - 5 Interaction using Q&A
25/02/2023	Session - 6 (R) Classic Paper of Flip-Flop Designs , How to pick right one and design	Session - 6 (RD) Classic Paper of Flip-Flop Designs , How to pick right one and design	Session - 6 Interaction using Q&A

Note:

- One explanation about the Patent filed
- Every session, last 15 minutes, discussion of a paper - optimised for power, performance

The inauguration was held on **20th February 2023** online at **7:00 pm**. Resource persons of the program **Mr. Arun Iyer** and **Anikethan**, Principal **Dr. Sanjay Pande M B**, Convener of FDP **Dr. Praveen J**, Coordinators **Mr. Sampath Kumar B** and **Mr. Harisha G C** and Participants were present.

Principal **Dr. Sanjay Pande M B** addressed the participants about online FDP and various initiatives by ATAL Academy. Convener of FDP **Dr. Praveen J** talked about FDP and explains guidelines of FDP, Resource persons of the program **Mr. Arun Iyer** Brief about Content of Online Sessions finally Coordinator **Mr. Sampath Kumar B** proposed the Vote of Thanks.



Srishyala Educational Trust (R), Bheemasamudra

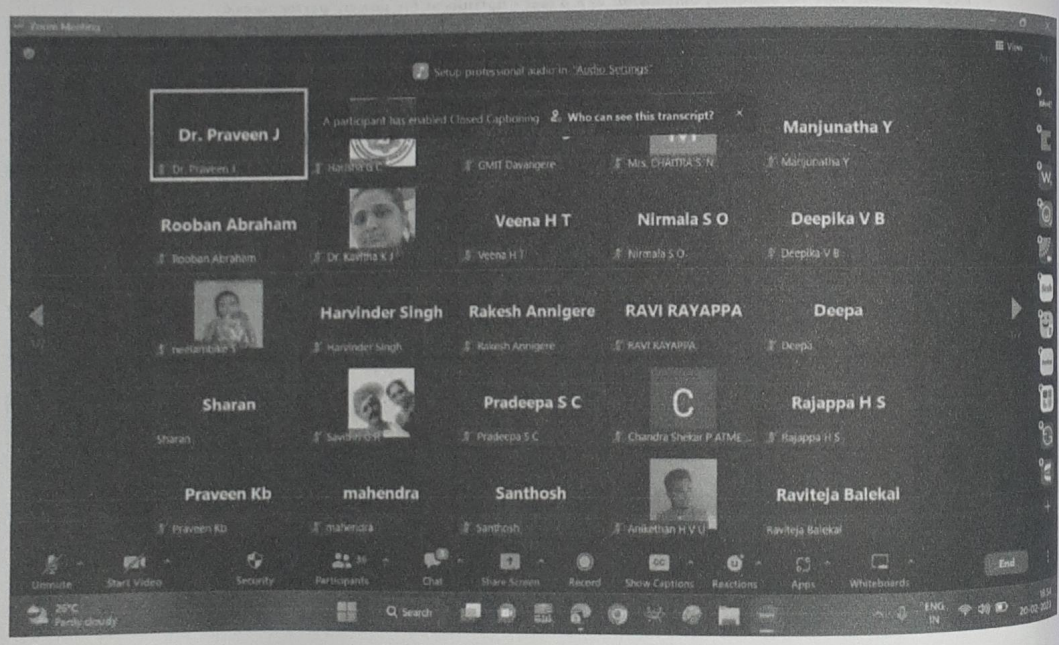
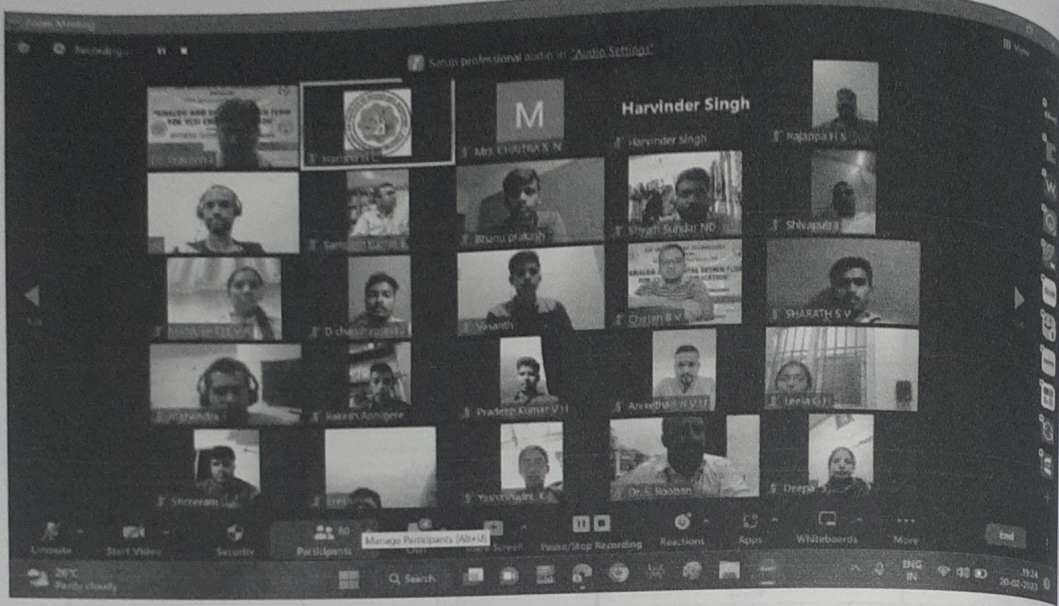
GM INSTITUTE OF TECHNOLOGY, DAVANGERE

Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SCREENSHOTS OF THE INAUGURAL FUNCTION



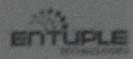


DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

OFFLINE MODE: 27th FEB to 3RD MAR 2023



AICTE TRAINING AND LEARNING (ATAL) ACADEMY
 Sponsored Two week
 Faculty Development Program on
"Analog and Digital Design Flow for VLSI Chip Application"
 Organized by
 Department of Electronics and Communication Engineering
 G M INSTITUTE OF TECHNOLOGY, Davangere-06
 27th Feb to 3rd March 2023 - Offline Mode
 Week - 2 OFFLINE (9.30 AM to 5.15 PM)



Dates	9.00 AM to 9.30 AM	10.00 AM to 1.00 PM	1.00 PM to 2.00 PM	2.00 PM to 4.00 PM	4.30 PM to 5.15 PM
27/02/2023 (Day-1)	Inauguration	(Session - 7) Introduction to Digital IC Design	Lunch	(Session - 8) Cadence EDA Tools for RTL-GDSII	
Dates	9.30-12.00 PM	12.00-1.00 PM	1.00 PM to 2.00 PM	2.00-4.00 PM	4.00-5.15 PM
28/02/2023 (Day-2)	(Session - 9) LEON Processors Design	Article 1 Discussion "Improving performance of the LEON architectures", Microelectronic Journal (2022), Elsevier	Lunch	(Session - 10) LEON Processors RTL-GDSII flow (Case Study - SoC)	
1/03/2023 (Day-3)	(Session - 11) MOSFET Characterization & Inverter Design	Article 2 Discussion "Trending IC design directions in 2022," Journal of Semiconductors (2022)	Lunch	(Session - 12) Hands-on Training EDA/IC Inverter Design From schematic to GDSII	
2/03/2023 (Day-4)	(Session - 13) MOS Amplifier Design an Intuitive Approach	Reflection Journal and Feedback (IC Design FFA Analysis)	Lunch	(Session - 14) Design of Single-stage Amplifier using Cadence Virtuoso tool (Case Study - Amplifier)	
3/03/2023	Travel for Industrial Visit		Lunch	MCOs	Voluntary

FDP Coordinator	FDP Co-coordinator
Dr. Praveen J Professor, IQAC Director & Head, Department of E & CE, G M Institute of Technology, Davangere E-mail ID: dr.praveenj@gm.it.ac.in Alternate E-mail ID: Praveen_vishruth@yahoo.com Mobile : 8884767555	Mr. Sampath Kumar B Assistant Professor, Department of E & CE, G M Institute of Technology, Davangere E-mail ID: sampathkumar@gm.it.ac.in Mobile : +919538617842
	Mr. Harisha G C Assistant Professor, Department of E & CE, G M Institute of Technology, Davangere E-mail ID: harishagc@gm.it.ac.in Mobile : +918792337913

The inauguration was held on 27th February 2022 Offline at 9:00 am. Chief Guest of the function **Sri. Suresh Patil Senior Director Marvell Semiconductor, India**, Resource persons of the program **Mr. Navaneeth K**, Principal **Dr. Sanjay Pande M B**, Convener of FDP **Dr. Praveen J**, Dean academic **Dr. Sunil Kumar B S**, Coordinators **Mr. Sampath Kumar B** and **Mr. Harisha G C** and Participants were present.

Convener of FDP **Dr. Praveen J** talked about FDP and explains guidelines of FDP, Dean academic **Dr. Sunil Kumar B S** talked about NEP and its implementation, Chief Guest of the function **Sri. Suresh Patil** talked about Scope and Opportunities of VLSI, Principal **Dr. Sanjay Pande M B** addressed the participants about importance of FDPs for participants to upgrade the trend of VLSI, finally Coordinator **Mr. Sampath Kumar B** proposed the Vote of Thanks



Shrilya Educational Trust (R), Bhoemasamudra

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

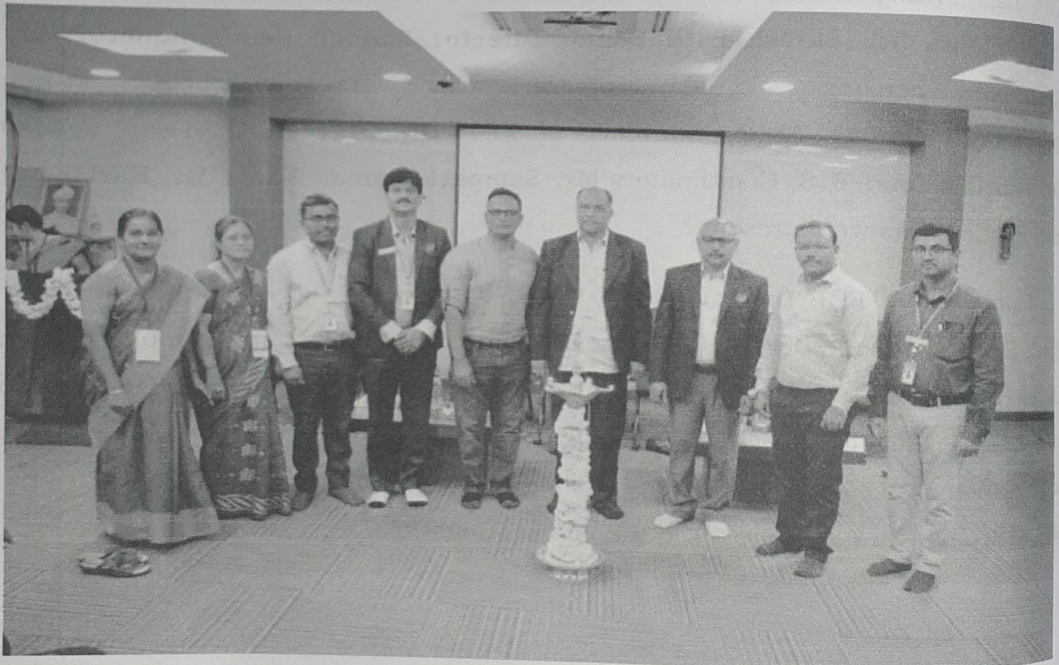
Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka

NBA
NATIONAL BOARD
OF ACCREDITATION
Accredited Programs
CSE/ECE/CE/AE/ISE/IT



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

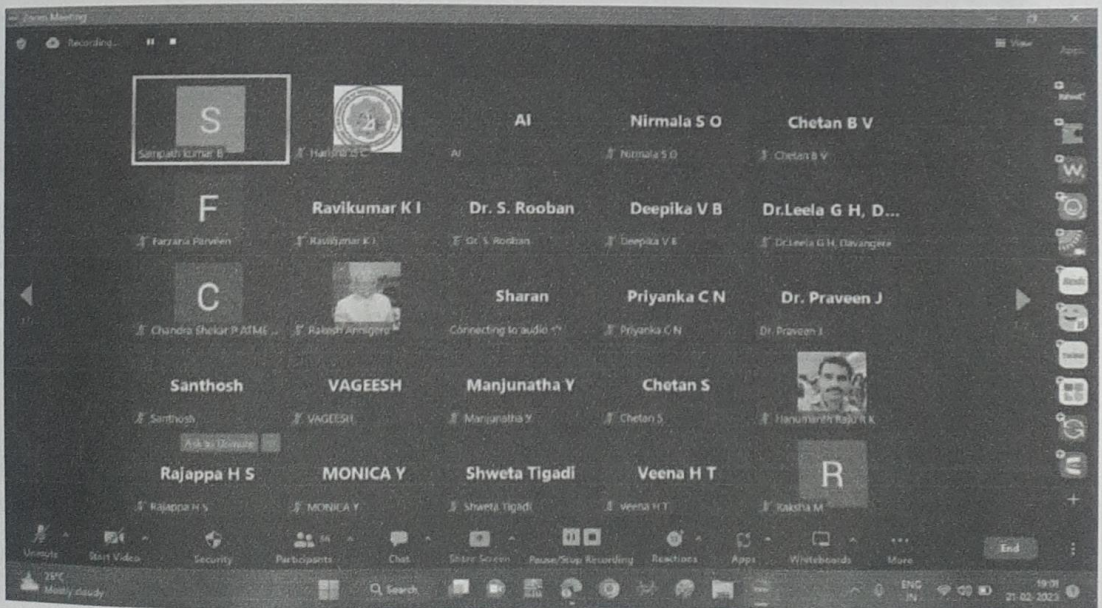
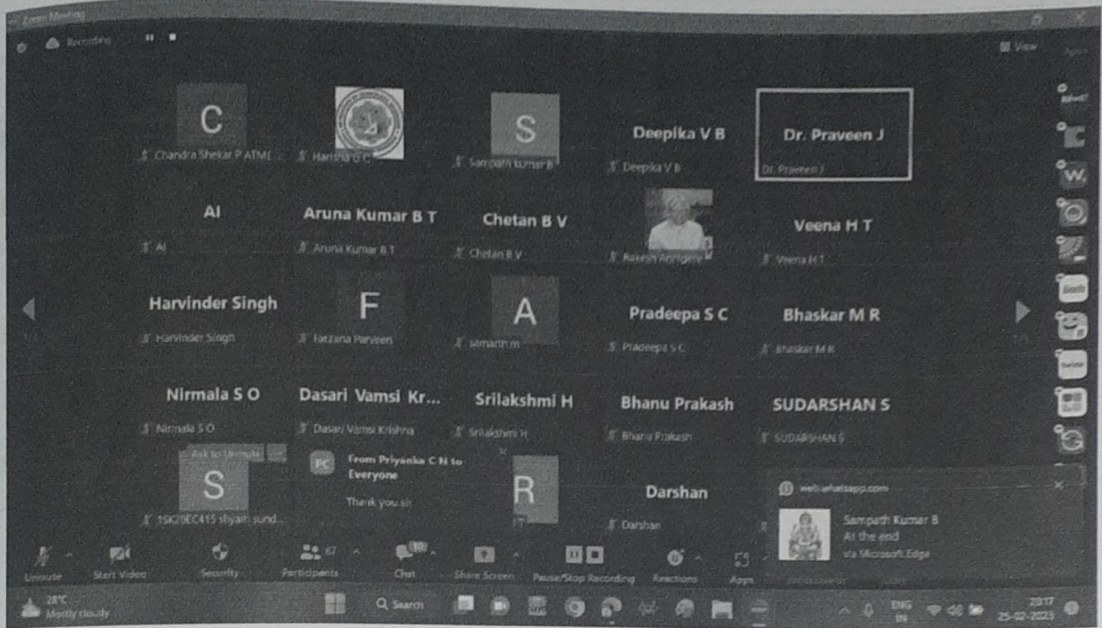
PHOTOS OF THE INAUGURAL FUNCTION





DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ONLINE SESSIONS PHOTOS





Sri Shyls Educational Trust (R), Rheemasamudra

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka

NBA
NATIONAL BOARD
OF ACCREDITATION
Accredited Programs
CSE/ECE/EEE/IT/BIOT



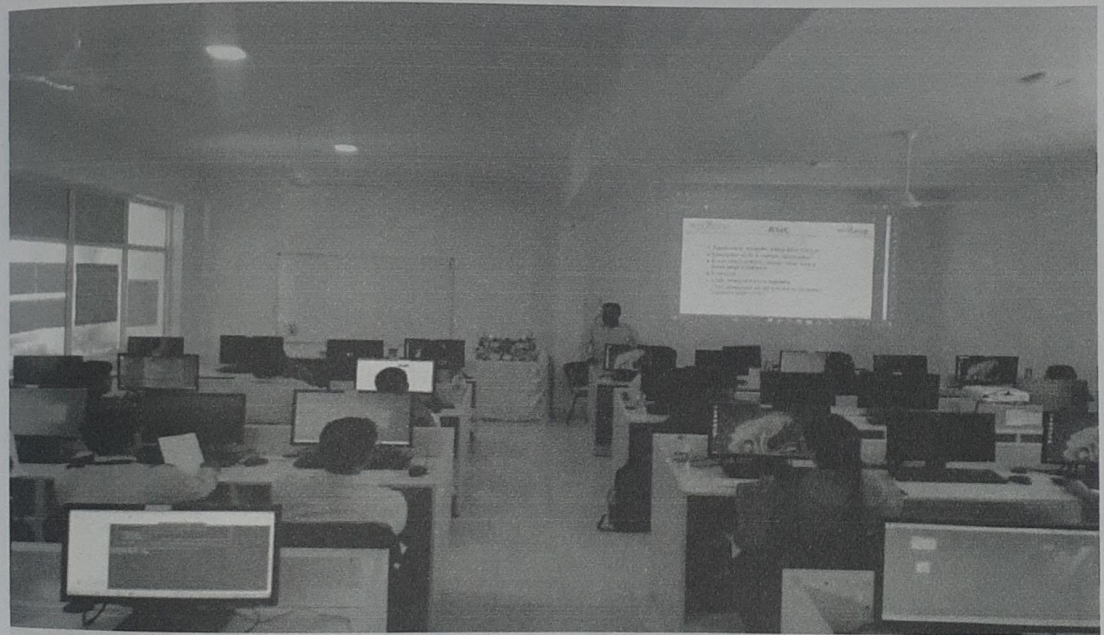
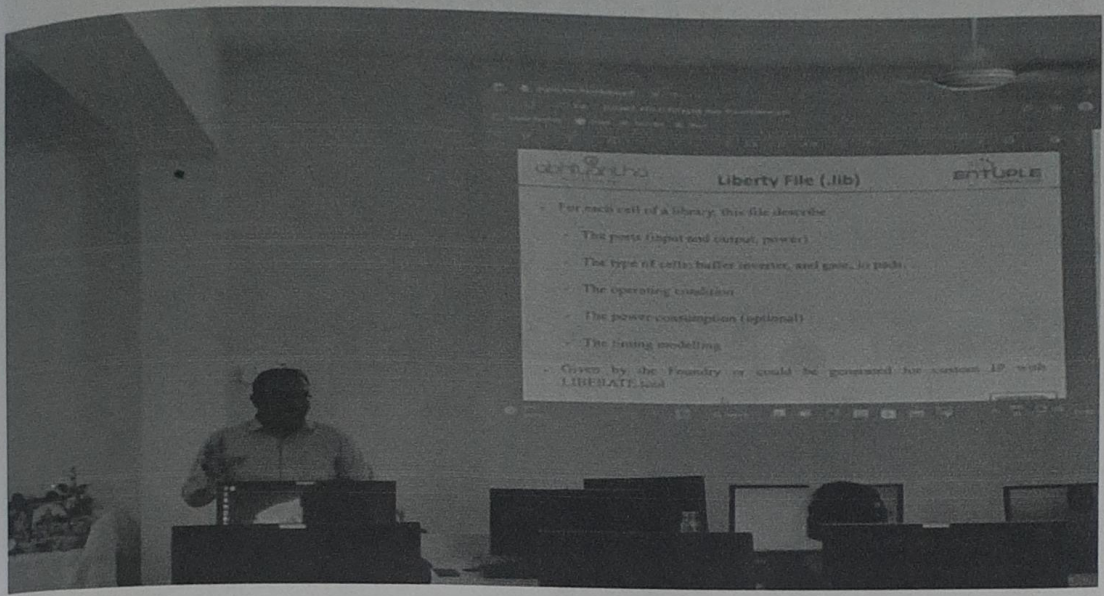
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

LINK FOR LECTURE VIDEOS OF ONLINE SESSIONS

Sl. No.	Date	YouTube Link
1	20/02/2023	https://www.youtube.com/watch?v=xIKRMtZtLck AICTE TRAINING AND LEARNING (ATAL) SPONSORED FDP DAY 1 CONTD..... - YouTube
2	21/02/2023	AICTE TRAINING AND LEARNING (ATAL) SPONSORED FDP DAY 2 - YouTube
3	22/02/2023	AICTE TRAINING AND LEARNING (ATAL) SPONSORED FDP - 22.02.2023 DAY 3 - YouTube
4	23/02/2023	AICTE TRAINING AND LEARNING (ATAL) SPONSORED FDP - 23.02.2023 DAY 4 - YouTube
5	24/02/2023	AICTE TRAINING AND LEARNING (ATAL) SPONSORED FDP 24.2.2023 DAY 5 - YouTube
6	25/02/2023	AICTE TRAINING AND LEARNING (ATAL) SPONSORED FDP 25.2.2023 DAY 6 - YouTube

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

OFFLINE SESSIONS PHOTOS





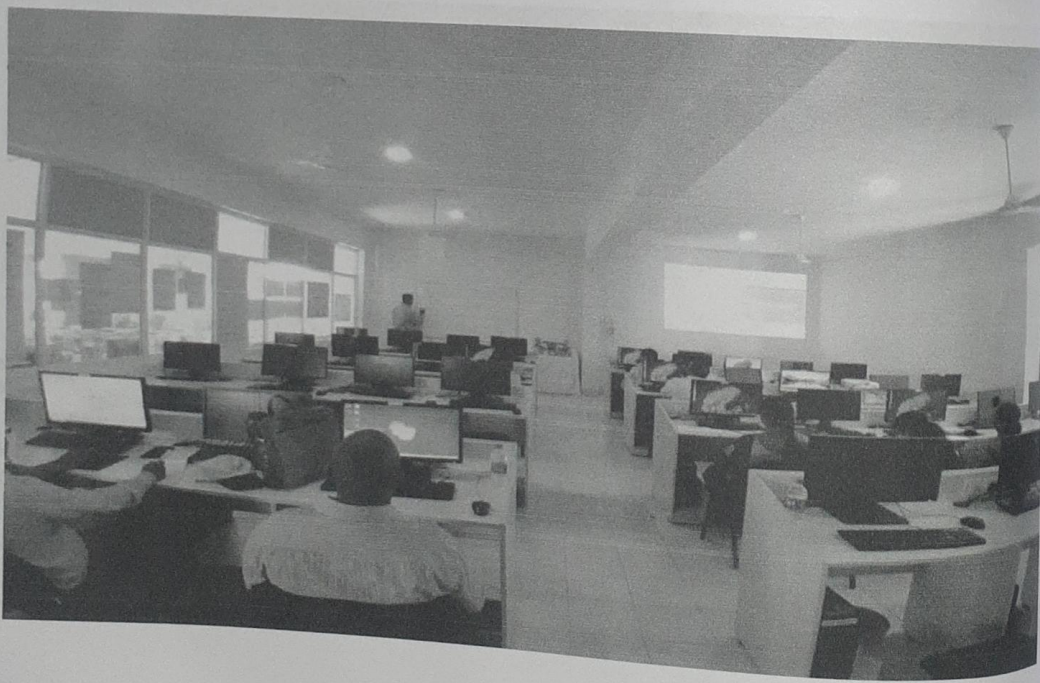
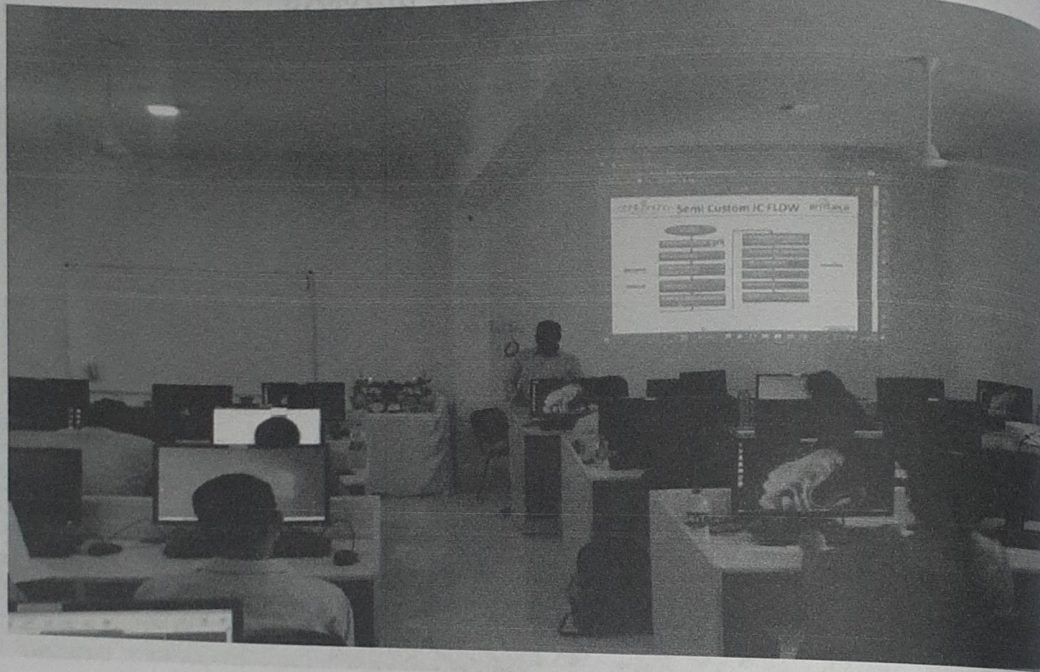
Srishya Educational Trust (R), Bheemasamudra

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

INDUSTRIAL VISIT PHOTOS





Shrilya Educational Trust (R), Bheemasamudra

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka

NBA
NATIONAL BOARD
of ACCREDITATION
Accredited Programs
CSE/ECE/CE/ME/ISE/IT



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING





Brahmika Educational Trust (R), Bhuvanagiri

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka

NBA
NATIONAL BOARD
OF ACCREDITATION
ACCREDITED PROGRAM
CSE/ECCE/EEE/EEE



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

PHOTOS TAKEN WHEN AWARDING BEST PERFORMERS OF THE FDP





DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING





Shrilya Educational Trust (R), Bheemasamudra

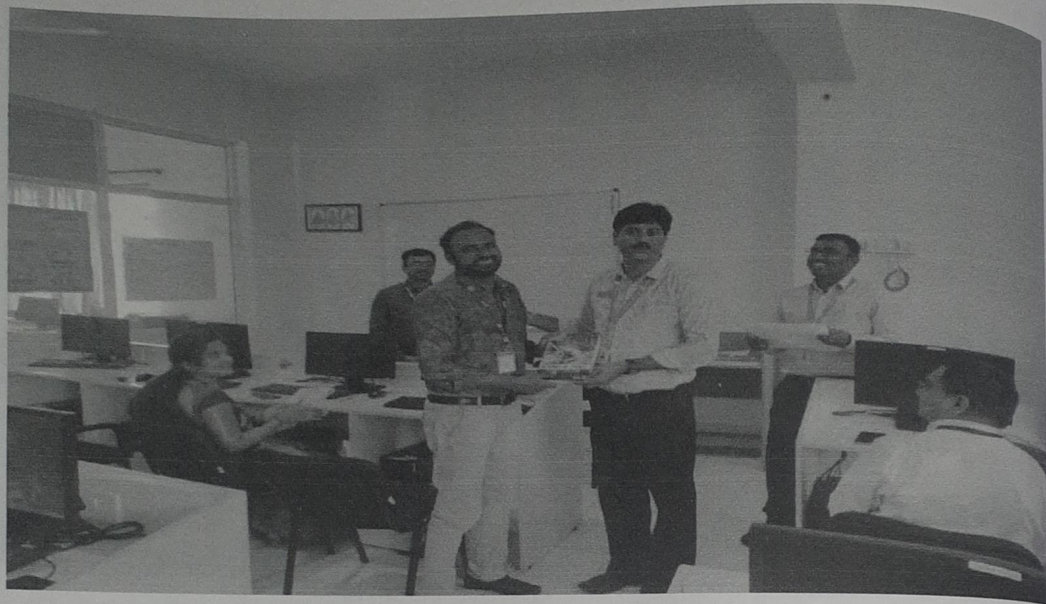
GM INSTITUTE OF TECHNOLOGY, DAVANGERE

Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka

NBA
NATIONAL BOARD
of ACCREDITATION
Accredited Programs
CSE/ECE/CE/ME/ISE/IT



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING





DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

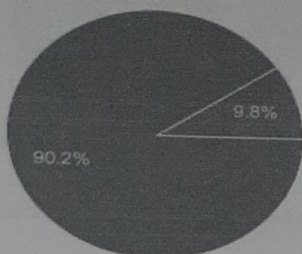
ONLINE SESSION FEEDBACK FROM PARTICIPANTS

Please, mark the relevant option for the following:

Organization of the online sessions in the FDP.

Copy

41 responses

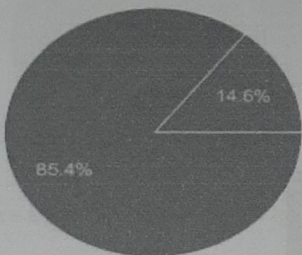


- Excellent
- Good
- Fair

Content delivered in the online sessions of the FDP.

Copy

41 responses

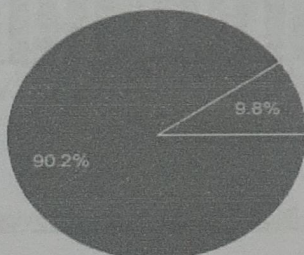


- Excellent
- Good
- Fair

Resource person preparation and presentation in the online sessions

Copy

41 responses



- Excellent
- Good
- Fair





Sriehyla Educational Trust (R), Bheemasamudra

GM INSTITUTE OF TECHNOLOGY, DAVANGERE

Approved by AICTE | Affiliated by V.T.U Belagavi | Recognized by Govt. of Karnataka

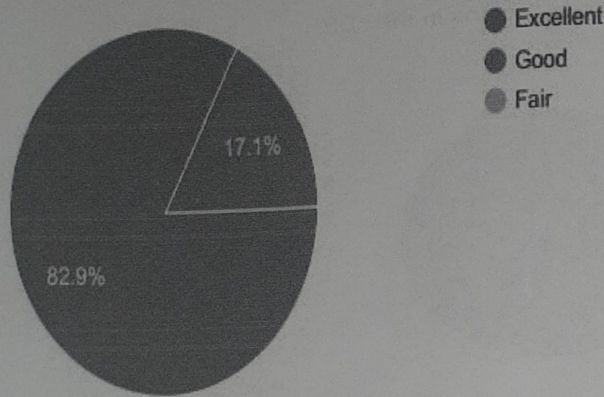


DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Resource person response to participants in the online session

Copy

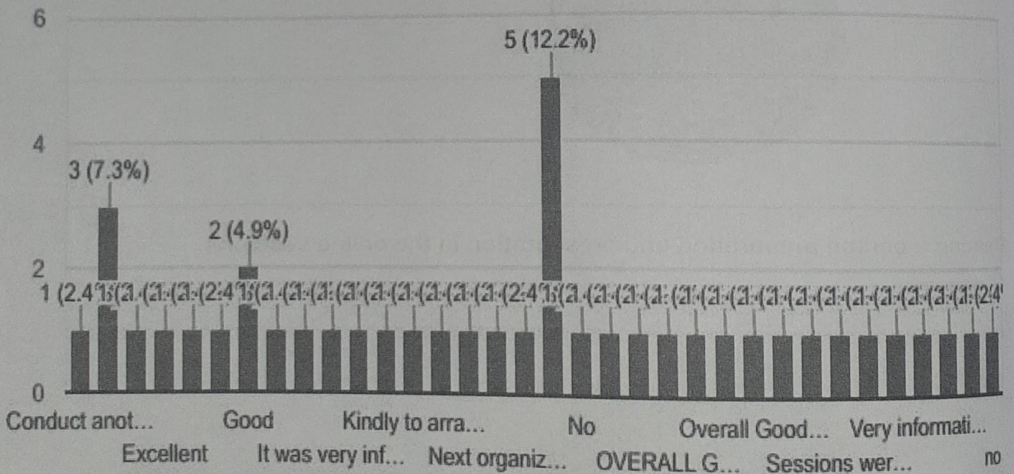
41 responses



Suggestions for improvement/Comments (if any)

Copy

41 responses



This content is neither created nor endorsed by Google. [Report Abuse](#) - [Terms of Service](#) - [Privacy Policy](#)

Google Forms



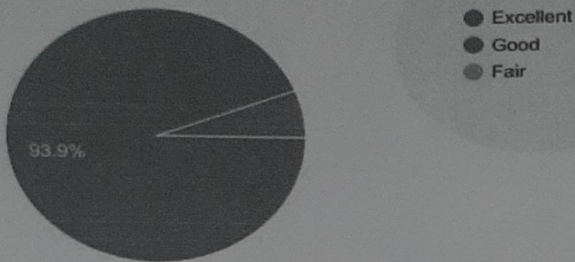
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

OFFLINE SESSION FEEDBACK FROM PARTICIPANTS

Content delivered in the offline sessions of the FDP.

Copy

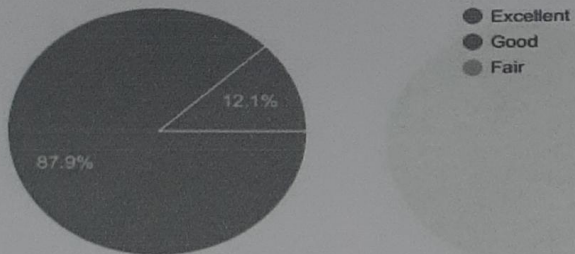
33 responses



Resource person preparation and presentation in the offline sessions

Copy

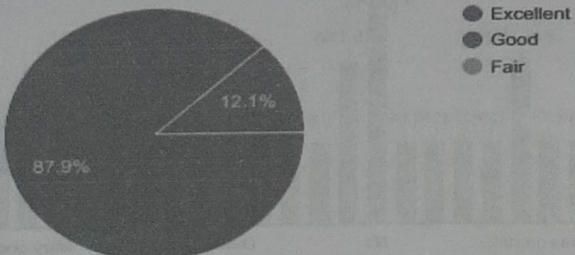
33 responses



Availability of resources in the hands-on sessions (includes tool, resource material etc.)

Copy

33 responses



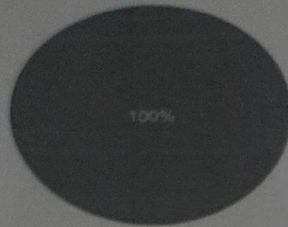


DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Organization of the offline sessions in the FDP.

Copy

33 responses

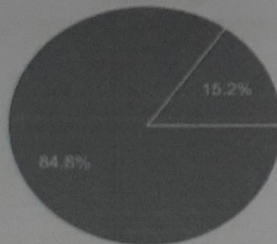


- Excellent
- Good
- Fair

Resource Persons response to participants in the offline sessions

Copy

33 responses

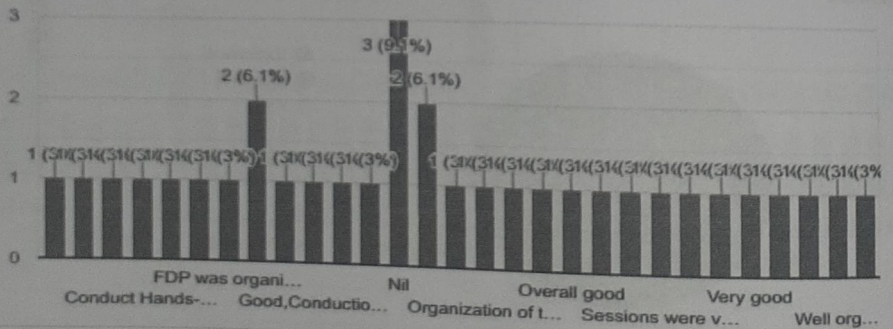


- Excellent
- Good
- Fair

Suggestions for improvement/Comments (if any)

Copy

33 responses





DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

It was a great Initiative by ATAL Academy. I am thankful to AICTE for giving me this opportunity to conduct FDP (blended mode) for faculty members of technical institute of India free of cost. I got huge response for registration as well as lots of compliment of arranging the online workshop, content and hands on.

ಜಿ.ಎಂ.ಐ.ಟಿ ಕಾಲೇಜಿನಲ್ಲಿ ಎ.ಐ.ಸಿ.ಟಿ.ಇ ಟ್ರೇನಿಂಗ್ ಅಂಡ್ ಲಿಫ್ಟಿಂಗ್ ಅಕಾಡೆಮಿ (ಅಟಲ್) ಪ್ರಾಯೋಜಿತ 2 ವಾರಗಳ ಎಫ್.ಡಿ.ಪಿ



ವಾರಗಳಿಗೆ: ಜಿ.ಎಂ.ಐ.ಟಿ ಕಾಲೇಜಿನ ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ ಅಂಡ್ ಕಮ್ಯುನಿಕೇಶನ್ ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗದಲ್ಲಿ ಎ.ಐ.ಸಿ.ಟಿ.ಇ ಟ್ರೇನಿಂಗ್ ಅಂಡ್ ಲಿಫ್ಟಿಂಗ್ ಅಕಾಡೆಮಿ (ಅಟಲ್) ಪ್ರಾಯೋಜಿತ ಎರಡು ವಾರಗಳ ಆನ್‌ಲೈನ್ ಆಫ್ ಲೈನ್ ಎಫ್.ಡಿ.ಪಿ ಪ್ರಾರಂಭಗೊಂಡಿದ್ದು ನಾಂಕು 20.02.2023 ರಂದು ಆನ್‌ಲೈನ್ ನಲ್ಲಿ ದೃಢೀಕರಿಸಲಾಯಿತು. ಈ ಕಾರ್ಯಕ್ರಮವು ದಿನಾಂಕ 02.02.2023 ರಿಂದ 25.02.2023 ರವರೆಗೆ ಆನ್‌ಲೈನ್ ಮಾಧ್ಯಮದಲ್ಲಿ ನಡೆದಿದ್ದು ಹಾಗೂ ದಿನಾಂಕ 02.02.2023 ರಿಂದ 03.03.2023 ರವರೆಗೆ ಆಫ್‌ಲೈನ್ ಮಾಧ್ಯಮದಲ್ಲಿ ನಡೆಯಲಿದೆ. ಕಾರ್ಯಕ್ರಮದ ಮುಖ್ಯ ಉದ್ದೇಶ "ಅನಲಾಗ್ ಅಂಡ್ ಡಿಜಿಟಲ್ ಡಿಜೈನ್ ಫಾರ್ ಎ.ಎಲ್.ಎಸ್.ಐ ಚಿಪ್ ಅಪ್ಲಿಕೇಶನ್" ಆಗಿರುತ್ತದೆ.

ಈ ಕಾರ್ಯಕ್ರಮವು ಎ.ಐ.ಸಿ.ಟಿ.ಇ, ನ್ನು ಉದ್ದೇಶಿಸಿ ಪ್ರಾಯೋಜಿಸಲಾಗಿದ್ದು, ಎನ್‌ಟುಪಲೆ, ಬೆಂಗಳೂರು ಇವರ ಸಹಯೋಗದೊಂದಿಗೆ ನಡೆಯಲಿರುತ್ತದೆ. ದಿನಾಂಕ: 27.02.2023 ರಂದು ಕಾರ್ಯಕ್ರಮದ ಆಫ್‌ಲೈನ್ ಉದ್ಘಾಟನೆ ಕಾಲೇಜಿನ ಎ.ಐ.ಸಿ.ಟಿ.ಇ ವಿಭಾಗದ ಆಡಿಟೋರಿಯಂನಲ್ಲಿ ನಡೆಯಿತು. ಅಧ್ಯಕ್ಷರಾದ ಡಾ|| ಸುನೀಲ್ ಕುಮಾರ್ ಬಿ.ಎಸ್. ಅತಿಥಿ. ಸುರೇಶ್ ಪಾಟೀಲ್, ಉಪಾಧ್ಯಕ್ಷರಾದ ಡಾ|| ಎಂ.ಎಂ.ಎಸ್.ಎಸ್.ಎಚ್. ಸಮೀಕಂಡಕ್ಕರ್, ಬೆಂಗಳೂರು, ಸಂಪನ್ಮೂಲ ವ್ಯಕ್ತಿಯಾಗಿ ಆಗಮಿಸಿದ್ದು, ಸವನೀತ ಕೃಷ್ಣನ್, ಸೀನಿಯರ್ ಅಪ್ಲಿಕೇಶನ್ ಇಂಜಿನಿಯರ್, ಎನ್‌ಟುಪಲೆ, ಬೆಂಗಳೂರು, ಕಾಲೇಜಿನ ಉಪಾಧ್ಯಕ್ಷರಾದ ಹಾಗೂ ಕಾರ್ಯಕ್ರಮದ ಅಧ್ಯಕ್ಷರಾದ ಡಾ|| ಸಂಜಯ್ ಪಾಂಡೆ, ಅಕೆಡೆಮಿಕ್ ಡೀನ್ ಆದ ಡಾ|| ಸುನೀಲ್ ಕುಮಾರ್ ಬಿ.ಎಸ್. ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ ಅಂಡ್ ಕಮ್ಯುನಿಕೇಶನ್ ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗದ

ಮುಖ್ಯಸ್ಥರು ಹಾಗೂ ಕಾರ್ಯಕ್ರಮದ ಸಂಯೋಜಕರಾದ ಡಾ|| ಪ್ರವೀಣ್ ಜಿ., ಪ್ರೊ|| ಸಂಪತ್ ಕುಮಾರ್, ಪ್ರೊ|| ಹರೀಶ್ ಜಿ.ಸಿ, ಉಪಸ್ಥಿತರಿದ್ದರು.

ಡಾ|| ಪ್ರವೀಣ್ ಜಿ., ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ ಅಂಡ್ ಕಮ್ಯುನಿಕೇಶನ್ ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗದ ಮುಖ್ಯಸ್ಥರು ಹಾಗೂ ಕಾರ್ಯಕ್ರಮದ ಸಂಯೋಜಕರು ಮಾತನಾಡಿ ಕಾರ್ಯಕ್ರಮದ ಮುಖ್ಯ ಉದ್ದೇಶದ ಬಗ್ಗೆ ಸಂಪೂರ್ಣ ವಿವರಣೆ ನೀಡಿದರು. ಅನ್ಯ ಕಾಲೇಜುಗಳಿಂದ 50 ಪ್ರಾಧ್ಯಾಪಕರು ಭಾಗವಹಿಸಿದ್ದರು ಎಂದು ಡಾ|| ಪ್ರವೀಣ್ ಜಿ., ತಿಳಿಸಿರುತ್ತಾರೆ. ಅಕೆಡೆಮಿಕ್ ಡೀನ್ ಆದ ಡಾ|| ಸುನೀಲ್ ಕುಮಾರ್ ಬಿ.ಎಸ್, ಅವರು ಮಾತನಾಡಿ ಈಗಿನ ಎನ್.ಇ.ಪಿ. ಸಿಲೆಬಸ್ ಪ್ರಕಾರ ಅಧ್ಯಾಪಕರು ತಮ್ಮ ಕೌಶಲ್ಯಗಳನ್ನು ಹೆಚ್ಚಿಸಿಕೊಳ್ಳಬೇಕಾಗಿ ತಿಳಿಸಿದರು.

ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ ಅಂಡ್ ಕಮ್ಯುನಿಕೇಶನ್ ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗದ ವಿದ್ಯಾರ್ಥಿಗಳಾದ ಶ್ರೀಗೌರಿ, ಶ್ರೀಲಕ್ಷ್ಮಿ ಪ್ರಾರ್ಥನೆ ನೀಡಿ ಹಾಡಿದರು. ಪ್ರೊ|| ಪ್ರವೀಣ್ ಜಿ. ದೀಪಾ ಕಾರ್ಯಕ್ರಮದ ನಿರೂಪಣೆ ನಡೆಸಿಕೊಟ್ಟರು. ಡಾ|| ಕವಿತಾ ಕೆ.ಜೆ, ಸ್ವಾಗತ ಭಾಷಣ ಮಾಡಿದರು. ರೇವತಿ ಎಸ್.ಎನ್, ಮುಖ್ಯ ಅತಿಥಿಗಳ ಪರಿಚಯ ಮಾಡಿಕೊಟ್ಟರು. ಡಾ|| ಸಂಜಯ್ ಪಾಂಡೆ, ಪ್ರಾಂಶುಪಾಲರು, ಜಿ.ಎಂ.ಐ.ಟಿ ಕಾಲೇಜು, ಅವರು ಕಾರ್ಯಕ್ರಮದ ಅಧ್ಯಕ್ಷರ ಭಾಷಣ ಮಾಡಿ ಕಾರ್ಯಕ್ರಮದ ಸದುಪಯೋಗವನ್ನು ಪಡೆದುಕೊಳ್ಳುವಂತೆ ತಿಳಿಸಿದರು. ಪ್ರೊ|| ಸಂಪತ್ ಕುಮಾರ್ ಅವರು ವಂದನಾರ್ಪಣೆ ಮಾಡಿದರು.

ತೇಜಸ್ವಿ ಕಟ್ಟೀಮನಿ, ಪ್ಲೇಸಮೆಂಟ್ ಆಫೀಸರ್, ಕಾಲೇಜಿನ ಆಡಳಿತ ಮಂಡಳಿ ಮತ್ತು ಮ್ಯಾನೇಜಮೆಂಟ್ ರೆಪ್ರೆಜೆಂಟೇಟಿವ್ ಆದ ವೈ.ಯು ಸುಭಾಶ್‌ಚಂದ್ರ ಅವರು ಕಾರ್ಯಕ್ರಮದ ಬಗ್ಗೆ ಪ್ರಶಂಸೆ ವ್ಯಕ್ತಪಡಿಸಿದ್ದಾರೆ.

ಜಿ.ಎಂ.ಐ.ಟಿ ಕಾಲೇಜಿನಲ್ಲಿ ಎ.ಐ.ಸಿ.ಟಿ.ಇ ಟ್ರೇನಿಂಗ್ ಲರ್ನಿಂಗ್ ಅಕೆಡೆಮಿ (ಅಟಲ್) ಪ್ರಾಯೋಜಿತ 2 ವಾರಗಳ ಎಫ್.ಡಿ.ಪಿ



ಕಮ್ಯುನಿಕೇಶನ್ ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗದ ಮುಖ್ಯಸ್ಥರು, ಸಂಯೋಜಕರು ಡಾ|| ಪ್ರವೀಣ್ ಜಿ., ಪೊ|| ಸಂಪತ್ ಕುಮಾರ್, ಪೊ|| ಹರೀಶ ಜಿ.ಸಿ., ಉಪಸ್ಥಿತರಿದ್ದರು. ಡಾ|| ಪ್ರವೀಣ್ ಜಿ., ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ & ಕಮ್ಯುನಿಕೇಶನ್

ಗಳೂರು: ಫೆ.27 ಜಿ.ಎಂ.ಐ.ಟಿ ಕಾಲೇಜಿನ ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ & ಕಮ್ಯುನಿಕೇಶನ್ ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗದಲ್ಲಿ ಎ.ಐ.ಸಿ.ಟಿ.ಇ ಟ್ರೇನಿಂಗ್ & ಲರ್ನಿಂಗ್ ಅಕೆಡೆಮಿ (ಅಟಲ್) ಪ್ರಾಯೋಜಿತ 2 ವಾರಗಳ ಆನ್‌ಲೈನ್-ಆಫ್ ಲೈನ್ ಎಫ್.ಡಿ.ಪಿ ಪ್ರಾರಂಭಗೊಂಡಿದ್ದು ದಿನಾಂಕ ಫೆ.20 ರಿಂದ ಆನ್‌ಲೈನ್ ನಲ್ಲಿ ಉದ್ಘಾಟಿಸಲಾಯಿತು. ಕಾರ್ಯಕ್ರಮ ಫೆ.20-25 ರವರೆಗೆ ಆನ್‌ಲೈನ್ ಮಾಧ್ಯಮದಲ್ಲಿ ನಡೆಯಲಿದ್ದು, ಫೆ.27-03 ರವರೆಗೆ ಆಫ್‌ಲೈನ್ ಮಾಧ್ಯಮ ನಡೆಯಲಿದೆ.

ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗದ ಮುಖ್ಯಸ್ಥರು, ಕಾರ್ಯಕ್ರಮದ ಸಂಯೋಜಕರು ಮಾತನಾಡಿದರು ಮುಖ್ಯ ವಿಷಯದ ಬಗ್ಗೆ ಸಂಪೂರ್ಣ ವಿವರಣೆ ನೀಡಿದರು. ಅನ್ಯ ಕಾಲೇಜು ಗಳಿಂದ 50 ಪ್ರಾಧ್ಯಾಪಕರು ಭಾಗವಹಿಸಿದ್ದರು. ಡಾ|| ಪ್ರವೀಣ್ ಜಿ., ತಿಳಿಸುತ್ತಾರೆ.

ಮುಖ್ಯ ವಿಷಯ 'ಅನಲಾಗ್ ಅಂಡ್ ಡಿಜಿಟಲ್ ಸಿಗ್ನಲ್ ಪ್ರೊಸೆಸಿಂಗ್ ವಿ.ಎಲ್.ಎಸ್.ಐ ಚಿಪ್ ಕಮ್ಯುನಿಕೇಶನ್ ಆಗಿರುತ್ತದೆ. ಎ.ಐ.ಸಿ.ಟಿ.ಇ, ನ್ಯೂಡೆಲ್ಲಿಯಿಂದ ಪ್ರಾಯೋಜಿಸಲಾಗಿದ್ದು, ಎನ್‌ಟುಪಲೆ, ಬೆಂಗಳೂರು ವಿಶ್ವವಿದ್ಯಾನಿಲಯದೊಂದಿಗೆ ನೆರವೇರಿರುತ್ತದೆ. ಫೆ.27 ರಂದು ಆಫ್‌ಲೈನ್ ಉದ್ಘಾಟನೆ ಕಾಲೇಜಿನ ಎಂ.ಬಿ.ಎ ವಿಭಾಗದ ಮುಖ್ಯಸ್ಥರೊಂದಿಗೆ ನಡೆಯಿತು.

ಅಕೆಡೆಮಿಕ್ ಡೀನ್ ಡಾ|| ಸುನೀಲ್ ಕುಮಾರ್ ಬಿ.ಎಸ್, ಅವರು ಮಾತನಾಡಿ ಈಗಿನ ಎನ್.ಇ. ಪಿ.ಸಿಲೆಬಸ್ ಪ್ರಕಾರ ಅಧ್ಯಾಪಕರು ತಮ್ಮ ಕೌಶಲ್ಯಗಳನ್ನು ಹೆಚ್ಚಿಸಿಕೊಳ್ಳಬೇಕಾಗಿ ತಿಳಿಸಿದರು.

ವೇದಿಕೆ ಮೇಲೆ ಮುಖ್ಯ ಅತಿಥಿಗಳಾದ ಶ್ರೀ. ಸುನೀಶ್ ಪಾಟೀಲ್, ಹಿರಿಯ ನಿರ್ದೇಶಕರು, ಸಿ.ಆರ್.ಪೆಲ್ ಸೆಮಿ ಕಂಡಕ್ಟರ್, ಬೆಂಗಳೂರು, ಸಂಪನ್ಮೂಲ ಇಲಾಖೆಯಾಗಿ ಆಗಮಿಸಿದ್ದ ಶ್ರೀ. ವಿ. ನವನೀತ ಕೃಷ್ಣನ್, ಡಿ.ಎಂ.ಆರ್ ಅಪ್ಲಿಕೇಶನ್ ಇಂಜಿನಿಯರ್, ಎನ್‌ಟುಪಲೆ, ಬೆಂಗಳೂರು, ಕಾಲೇಜಿನ ಪ್ರಾಂಶುಪಾಲರು, ಅಧ್ಯಕ್ಷರು ಸಂಜಯ್ ಪಾಂಡೆ, ಅಕೆಡೆಮಿಕ್ ಡೀನ್, ಡಾ|| ಸುನೀಲ್ ಕುಮಾರ್ ಬಿ.ಎಸ್, ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ ಅಂಡ್

ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ & ಕಮ್ಯುನಿಕೇಶನ್ ಇಂಜಿನಿಯರಿಂಗ್ ವಿಭಾಗದ ವಿದ್ಯಾರ್ಥಿಗಳಾದ ಕು. ಶ್ರೀಗೌರಿ, ಕು. ಶ್ರೀಲಕ್ಷ್ಮಿ, ಪ್ರಾರ್ಥನೆಗೀತೆ ಹಾಡಿದರು. ಪ್ರೊ.ಪೆಸರ್‌ಗಳಾದ ಶ್ರೀಮತಿ. ದೀಪಾ ಕಾರ್ಯಕ್ರಮದ ನಿರೂಪಣೆ ನಡೆಸಿಕೊಟ್ಟರು. ಡಾ|| ಕವಿತಾಕೆ.ಜಿ, ಸ್ವಾಗತ ಭಾಷಣ ಮಾಡಿದರು.

ಶ್ರೀಮತಿ ರೇವತಿಎಸ್.ಎನ್, ಮುಖ್ಯ ಅತಿಥಿಗಳ ಪರಿಚಯ ಮಾಡಿ ಕೊಟ್ಟರು. ಡಾ|| ಸಂಜಯ್ ಪಾಂಡೆ, ಪ್ರಾಂಶುಪಾಲರು, ಜಿ.ಎಂ.ಐ.ಟಿ ಕಾಲೇಜು, ಅಧ್ಯಕ್ಷರ ಭಾಷಣ ಮಾಡಿ ಸದುಪಯೋಗವನ್ನು ಪಡೆದುಕೊಳ್ಳುವಂತೆ ತಿಳಿಸಿದರು. ಪೊ|| ಸಂಪತ್ ಕುಮಾರ್ ಅವರು ವಂದನಾರ್ಪಣೆ ಮಾಡಿದರು. ಶ್ರೀ. ತೇಜಸ್ವಿ ಕಟ್ಟೀಮನಿ, ಪ್ರೊ. ಸಮಂಟ್ ಆಫೀಸರ್, ಕಾಲೇಜಿನ ಆಡಳಿತ ಮಂಡಳಿ ಮತ್ತು ಮ್ಯಾನೇಜಮೆಂಟ್ ಪ್ರಜೆಂಟೇಟಿವ್ ಶ್ರೀ. ವೈ.ಯು ಸುಭಾಶ್ ಚಂದ್ರ ಕಾರ್ಯಕ್ರಮದ ಬಗ್ಗೆ ಪ್ರಶಂಸೆ ವ್ಯಕ್ತಪಡಿಸಿದ್ದಾರೆ.

K
Y
M
C