

# Kargal S.Gurumurthy

K-41, Shriram Sadhana Apts  
M S R Road, Gokula, Bangalore-54

Tele: 9845120045

E-mail : drksgurumurthy@gmail.com

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## AREAS OF INTEREST

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- Low power VLSI
- Novel devices for VLSI
- Devices' Modelling, Quantum Cellular Automata
- Nano Electronics

## EDUCATION QUALIFICATIONS

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August 1985-  
September-1990

- **PhD Thesis Title: Electrical characteristics of Thin SiO<sub>2</sub> films for VLSI devices**
- **PhD –Degree awarded from Department of ECE, IISc, Bangalore-12, in September 1990**

1980-1982

- **M.E (Solid state Electronics):** University of Roorkee, 1982. Solid – Passed with I class (Hons). Also bagged **University gold medal** for securing Highest marks.

1968-1975

- **B.E (ECE):** MCE, Hassan, Mysore University, Passed in 1973 with I class

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## Teaching Experience

- **FEBRUARY 2018 –JUNE 2018:** Worked as ‘**Adjunct Professor**’, in the Department of ECE, School of Engineering ,PRESIDENCY UNIVERSITY, Bangalore-64
- **Jan 2017-Jan 2018:** Worked as a **Professor** in the Department of ECE, DSCE, BANGALORE-79
- **May 2013 –Dec2016:** Worked as a “**SENIOR PROFESSOR**”, in the department of ECE, REVE ITM, BANGALORE-64
- **1998-2013:** Worked as a **PROFESSOR**, in the Department of ECE, UVCE, BU, BANGALORE-1
- **1990-1998:** worked as an “**ASSOCIATE PROFESSOR**”, in the Department of ECE, UVCE, BU, BANGALORE-1
- **1983-1990:** Worked as an **Assistant Professor, in the Department of ECE, UVCE, BU, Bangalore-1**
- **1975-1980:** Worked as a “**Lecturer**” in the Department of EE, BMSCE, Bangalore-17
- **1973-75:** Worked as **GOVIT** at ITI Ltd, Bangalore

## Research and Publications:

- I have produced 10 PhDs from Bangalore University, Bangalore, 3 PhDs from Dr.MGR University, Chennai, 2 PhDs from JNTU, Hyderabad, and 4 PhDs from VTU Belgaum. (**Total-19**)
- Every student has published well. So there are nearly 150 technical

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publications published in the Journals and conferences.

- I have executed a project under TAPTEC of AICTE, Newdelhi, with a grant of Rs7.5 Lakhs.
- I have also worked as a co-investigator for a MODROBS project with a grant of Rs. 5Lakhs.

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**Technical talks delivered::**

- I have delivered over 100 technical talks on various topics of VLSI, and VLSI Design at different Engineering colleges, in Karnataka, A.P and TN. I have extensively participated in VLSI activities and contributed maximum to popularise the research on VLSI.
- Now my interests are into nano technology. I contemplate to collaborate with CeNSE, IISc, Bangalore-12. I wish to get actively involved with that centre and use the state of art facilities of that centre for R & D work.
- I was also a resource person for VTU EDUSAT's e-Learning program. I have delivered live lectures on ' Digital Design using VHDL' and ' Digital CMOS technology'

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**UPSC and NBA assignments:**

- I have been working as a subject expert for ECE, since 2004 on the NBA panel. I have visited nearly 50 colleges so far as part of accreditation process as a subject expert and also as a co-chair of the NBA visiting committee.
- I am well versed with the accreditation process. I have delivered talks on NBA in many colleges.
  - I have also worked as a paper setter and Valuer of UPSC's IES papers since2006. For the last 2 years I have worked as a head examiner for the paper –Telecom-II
  - I have presented papers in Japan, Malaysia, France and USA
  - I have produced 19 PhDs so far.
  - I have been an author and co-author for around 150 technical papers
  - I am a MIEE (Senior) and MISTE

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**PERSONAL DETAILS**

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|-----------------|---|
| Date of birth   | • 28 <sup>th</sup> January 1951   |
| Marital status  | • Married. My wife was a Zoology Professor in Government College. Having a son and a daughter |
| Nationality     | • Indian  |
| Languages known | • Kannada, English and Hindi  |
| Hobbies         | • Photography, music, sports  |
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**REFERENCES:**

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1. **Dr. Navakanth Bhat, Professor, Chairman, CeNSE, IISc, Bangalore-560012**
  2. **Dr. Ashok Saxena, Professor (Retired), Dept of Electronics and Computer science, IIT, Roorkee, Roorkee**
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I certify that all the information stated in my CV is true and complete to the best of my knowledge.

**Kargal S. Gurumurthy**

