

Faculty Profile - VTU FACULTY ID: 4GMEC0001408

Savita S Patil

Assistant Professor,
Department of Electronics & Communication Engineering,
GM Institute of Technology, Davangere



Vision

Your vision :To contribute to Society and GMIT through excellence in Technical Education

Educational Qualifications

1	M.Tech in VLSI & EMB
2	B.E

Professional Details

Total experience: 14 years (Teaching:14, Industry:0, Other:0)

Place of working	GMIT,Davangere	13.5Y
Worked Place	BIET,Davangere	0.5Y

Academic / Research Publications

1	Savita S Patil,Chaitra B H ,”Deployment of sensors to minimize problems inmobile sensor networks” International Journal for research trends and innovation ,Vol 2 issue 6, ISSN: 2456-3315 ,june 2017	International Journal
2	SMT. SAVITA S PATIL ,RAMYA ANNIGERE,”Convolution and De-convolution using Vedic Mathematics”, INTERNATIONAL JOURNAL OF ADVANCED RESEARCH IN COMPUTER AND COMMUNICATION ENGINEERING, IJARCC, Volume 5, Issue 7, July 2016 DOI 10.17148/IJARCC.2016.57118	International Journal

Awards / Achievements

1	Got 100% Results in VLSI,Digital VLSI Design and Basic Electronics,RTS,DVLSI Subjects.
---	---

Workshops / Seminars / Conference organized / Conducted

1	Latex For Documentation
---	-------------------------

Workshops / Seminars / Conference attended

1	Two days Workshop on “MSP430-MicroController” at GMIT Davangere.
2	Three days FDP onFeel Teacher by CHLRD at GMIT Davangere.
3	Two Days Workshop OnSystem Design Using Vivado Design Suite and Zynq-7000 SoC At BIET,Davangere.

University responsibilities

1	Practical Exam duty
2	University Question Paper setting
3	Paper Evaluation

Details of Academic e-Contents prepared / used

1	IEEE Xplore Digital Library
---	-----------------------------

Membership details

LMISTE	LM 43067
--------	----------

Personal Details

Gender	Female
Date of Birth	11-11-1978
Email	savitasp@gmit.ac.in
Contact detail	9900129964